PATENT ABSTRACTS OF JAPAN

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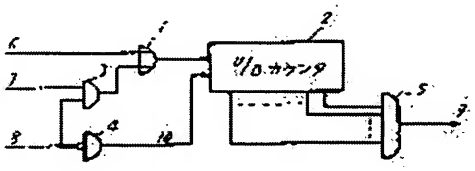
(54) INTERRUPTION PROCESSING CIRCUIT

(57)Abstract:

PURPOSE: To execute surely the processing for the number of times of interruption input by continuing the processing of an interruption cause to a data processing section until a count value of an up-down counter reaches a prescribed value when an interruption signal is inputted.



CONSTITUTION: When an interruption is generated successively and the next interruption signal enters from a terminal 6 until the processing of the first interruption cause is not finished, an up/down counter 2 counts up as 1, 2 and when it receives a processing end signal 8 processing the initial interruption cause from a microprocessor on its way, the count is decremented in synchronizing with a clock 7, the count value is restored to 1, and the counter counts up again by using the interruption signal given succeedingly and the count value becomes 2. Thus, the presence of interruption cause to be processed continuously is communicated to the microprocessor from a terminal 9. Since this notice is continued until the count value of the counter 2 reaches 0, the microprocessor accomplishes surely the processing corresponding to the number of times of input of the interruption signal.

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